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PATENT
081225.024 5694



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:
KUSUNOKI, et al.

Serial No.: Not Assigned

Filed: March 20, 2001

For: METHOD OF SIMULATION OF
PRODUCTION PROCESS OF
SEMICONDUCTOR DEVICE AND
SIMULATOR THEREOF

Group No.: Not Assigned

Examiner: Not Assigned

Assistant Commissioner For Patents
Washington, D.C. 20231

TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT

Dear Sirs:

The information disclosure statement submitted herewith is being filed concurrently with the subject application [37 C.F.R. §1.97(b)].

If it should be determined that for any reason either an insufficient or an excessive fee has been paid, please charge any insufficiency or credit any overpayment necessary to ensure consideration of the information disclosure statement for the above-identified application to Deposit Account No. 03-3975. A copy of this petition is enclosed.

Respectfully submitted,

PILLSBURY WINTHROP LLP

Dated: March 20, 2001

By: _____

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FORM PTO-1449 INFORMATION DISCLOSURE CITATION IN AN APPLICATION <i>(Use several sheets if necessary)</i>										Docket Number (Optional) 081225.024 5694				Application No. Not Assigned			
										Applicant KUSUNOKI, et al.							
										Filing Date March 20, 2001				Group Art Unit Not Assigned			

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U.S. PATENT DOCUMENTS																	
EXAMINER INITIAL	DOCUMENT NUMBER								DATE	NAME	CLASS	SUBCLAS S	FILING DATE IF APPROPRIATE				

FOREIGN PATENT DOCUMENTS														
	DOCUMENT NUMBER								DATE	COUNTRY	CLASS	SUBCLAS S	Translation	
													Yes	No
	1	0	3	1	2	4	8	02/09/99	Japan				X	
	0	3	2	7	6	1	6	11/28/97	Japan				X	

OTHER DOCUMENTS <i>(Including Author, Title, Date, Pertinent Pages, Etc.)</i>													

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP §609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.